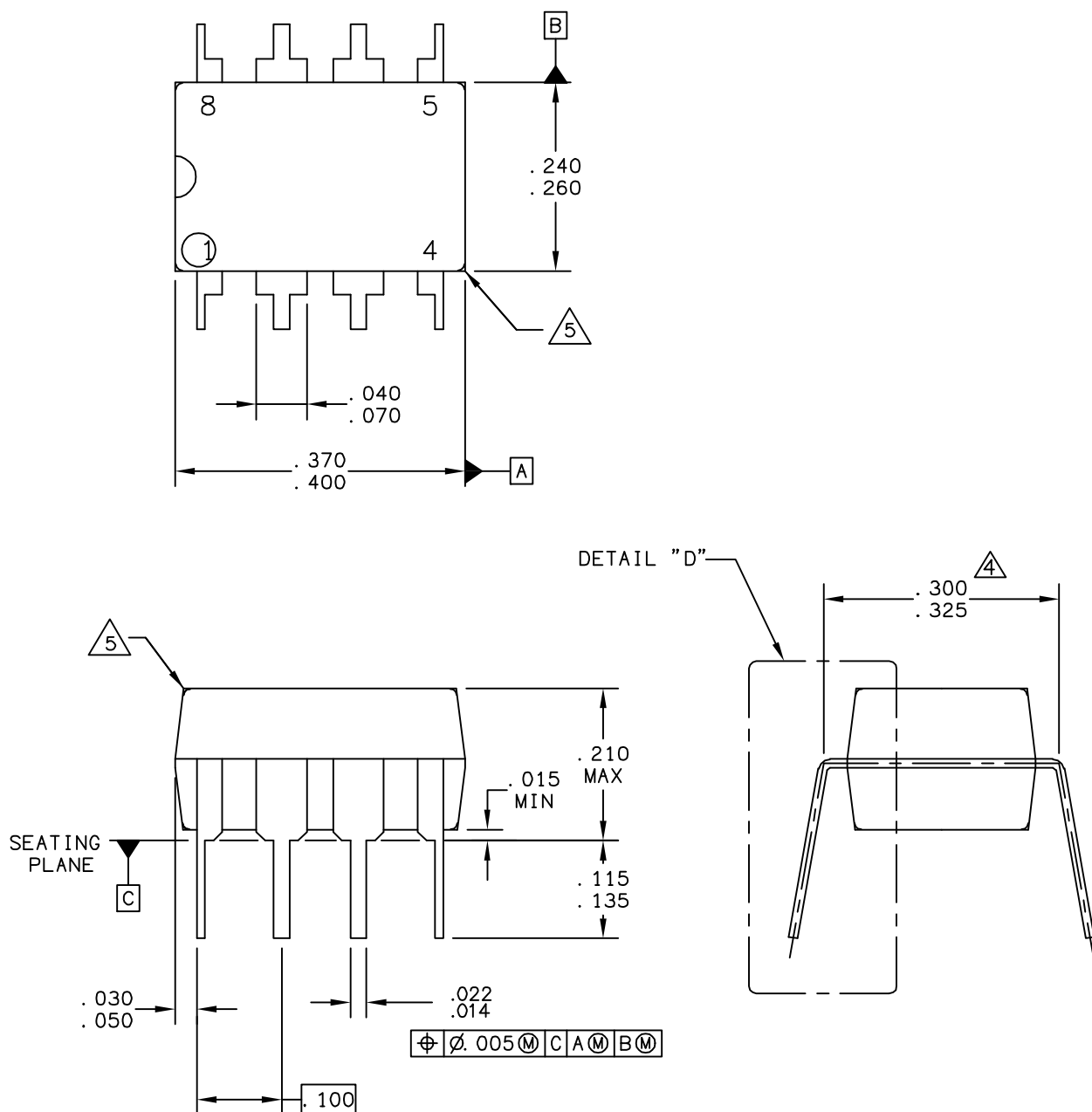
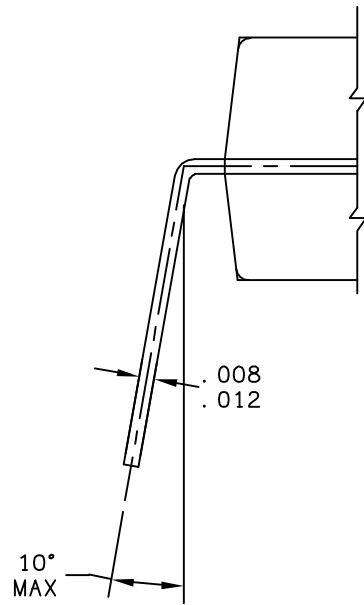




|                                                         |  |                           |                          |                            |             |
|---------------------------------------------------------|--|---------------------------|--------------------------|----------------------------|-------------|
| © FREESCALE SEMICONDUCTOR, INC.<br>ALL RIGHTS RESERVED. |  | <b>MECHANICAL OUTLINE</b> |                          | PRINT VERSION NOT TO SCALE |             |
| TITLE:<br><br>8 LD PDIP                                 |  |                           | DOCUMENT NO: 98ASB42420B |                            | REV: N      |
|                                                         |  |                           | CASE NUMBER: 626-06      |                            | 19 MAY 2005 |
|                                                         |  |                           | STANDARD: NON-JEDEC      |                            |             |



DETAIL "D"

|                                                         |                          |  |                            |  |
|---------------------------------------------------------|--------------------------|--|----------------------------|--|
| © FREESCALE SEMICONDUCTOR, INC.<br>ALL RIGHTS RESERVED. | MECHANICAL OUTLINE       |  | PRINT VERSION NOT TO SCALE |  |
| TITLE:<br><br>8 LD PDIP                                 | DOCUMENT NO: 98ASB42420B |  | REV: N                     |  |
|                                                         | CASE NUMBER: 626-06      |  | 19 MAY 2005                |  |
|                                                         | STANDARD: NON-JEDEC      |  |                            |  |

NOTES:

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M – 1994.
2. ALL DIMENSIONS ARE IN INCHES.
3. 626-03 TO 626-06 OBSOLETE. NEW STANDARD 626-07.

 DIMENSION TO CENTER OF LEAD WHEN FORMED PARALLEL.

 PACKAGE CONTOUR OPTIONAL (ROUND OR SQUARE CONERS).

STYLE 1:

|     |    |         |    |           |
|-----|----|---------|----|-----------|
| PIN | 1. | AC IN   | 5. | GROUND    |
|     | 2. | DC + IN | 6. | OUTPUT    |
|     | 3. | DC – IN | 7. | AUXILIARY |
|     | 4. | AC IN   | 8. | VCC       |

© FREESCALE SEMICONDUCTOR, INC.  
ALL RIGHTS RESERVED.

**MECHANICAL OUTLINE**

PRINT VERSION NOT TO SCALE

TITLE:

8 LD PDIP

DOCUMENT NO: 98ASB42420B

REV: N

CASE NUMBER: 626-06

19 MAY 2005

STANDARD: NON-JEDEC