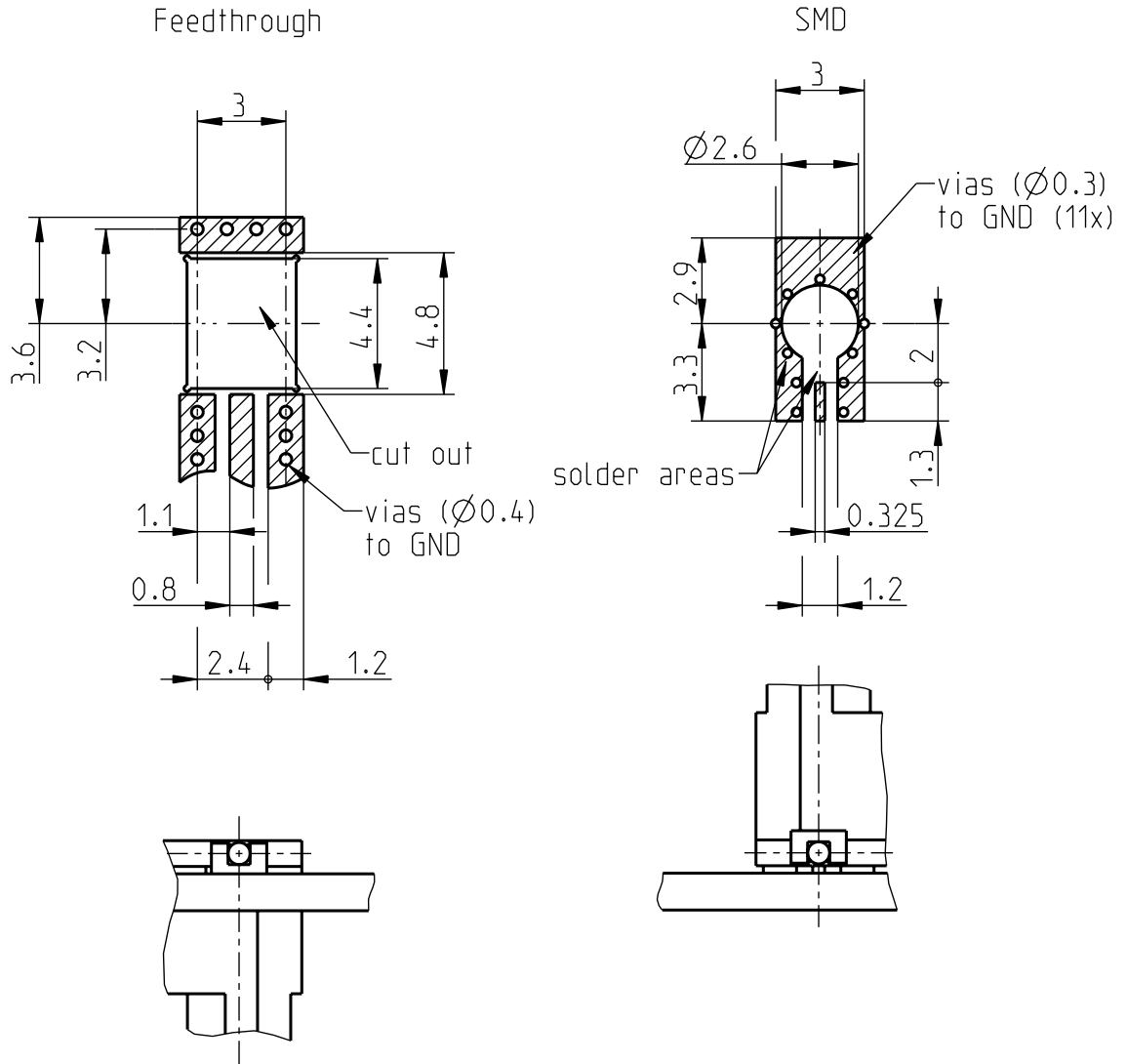


Leiterplatten-Layout
 PCB layout
 B 501



A wide variety of transmission line topologies and pcb-parameters like permittivity, substrate thickness, and board-stack up are applied by customers. These parameters have a strong impact on the high frequency performance of the mounted connector. Please note, that the given layout is not optimised to fit all of the possible board configurations regarding RF-performance, it represents a recommendation for optimum solderability of the connector. In order to guarantee optimum high frequency properties of the connector, an RF-analysis of the connector to board transition is recommended.

Rosenberger		general tolerance		scale: 4:1 ()		weight[g]: 8.384					
		ISO 2768	RN 006-01	material:		surface[mm ²]: 2151.0					
		mH	dimensions <0.5 and symmetry		Leiterplatten-Layout PCB layout						
		date	name					drawing-no...: MB_501			
		drawn	11.09.2014 S_Andorfer							sheet: 1 of: 1	
		check.	17.09.2014 G_Schiele								
		appr.	17.09.2014 M_Moder		remarks: .						
		dimensioning incl. plating									
		Size ISO 14405 (E)			drawing-no...: MB_501						
		Tolerancing ISO 8015									
a00	14-1309	S_Andorfer	17.09.2014								
rev.	change-no	name	date								