

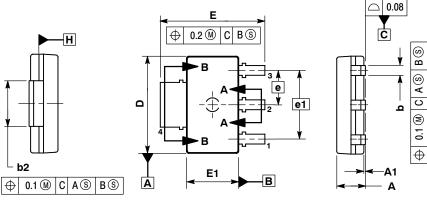
SOT-223 **CASE 318H-01 ISSUE O**

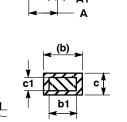
DATE 04/04/1997



(b2)

SECTION B-B





SECTION A-A

- NOTES:
 1. DIMENSIONS ARE IN MILLIMETERS.
- 2. INTERPRET DIMENSIONS AND TOLERANCES PER ASME Y14.5M, 1994.
- DIMENSION E1 DOES NOT INCLUDE INTERLEAD FLASH OR PROTRUSION. INTERLEAD FLASH OR PROTRUSION SHALL NOT EXCEED 0.23 PER
- SIDE.

 4. DIMENSIONS 6 AND 62 DO NOT INCLUDE DAMBAR PROTRUSION. ALLOWABLE DAMBAR PROTRUSION SHALL BE 0.08 TOTAL IN EXCESS OF THE 6 AND 62 DIMENSIONS AT MAXIMUM MATERIAL CONDITION.
- 5. TERMINAL NUMBERS ARE SHOWN FOR REFERENCE ONLY.
- 6. DIMENSIONS D AND E1 ARE TO BE DETERMINED AT DATUM PLANE H.

	MILLIMETERS		
DIM	MIN	MAX	
Α		1.80	
A1	0.02	0.11	
b	0.60	0.88	
b1	0.60	0.80	
b2	2.90	3.10	
b3	2.90	3.05	
C	0.24	0.35	
c1	0.24	0.30	
D	6.30	6.70	
Ε	6.70	7.30	
E1	3.30	3.70	
е	2.30		
e1	4.60		
L	0.25		
\propto	0 °	10°	

DOCUMENT NUMBER:	98ASH70634A	Electronic versions are uncontrolled except when accessed directly from the Document Repository. Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red.	
DESCRIPTION:	SOT-223		PAGE 1 OF 1

ON Semiconductor and are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. ON Semiconductor does not convey any license under its patent rights nor the rights of others.