

# **SOT684-16**

HVQFN56, plastic, thermally enhanced very thin quad flat, non-leaded package; 56 terminals; 0.5 mm pitch; 8 mm x 8 mm x 0.85 mm body

12 July 2019

**Package information** 

## 1 Package summary

Terminal position code Q (quad)

Package type descriptive code HVQFN56

Package type industry code HVQFN56

Package style descriptive code HVQFN (thermal enhanced very thin quad

flatpack; no leads)

Package body material type P (plastic)

Mounting method type S (surface mount)

Issue date 12-07-2019

Manufacturer package code 98ASA00405D

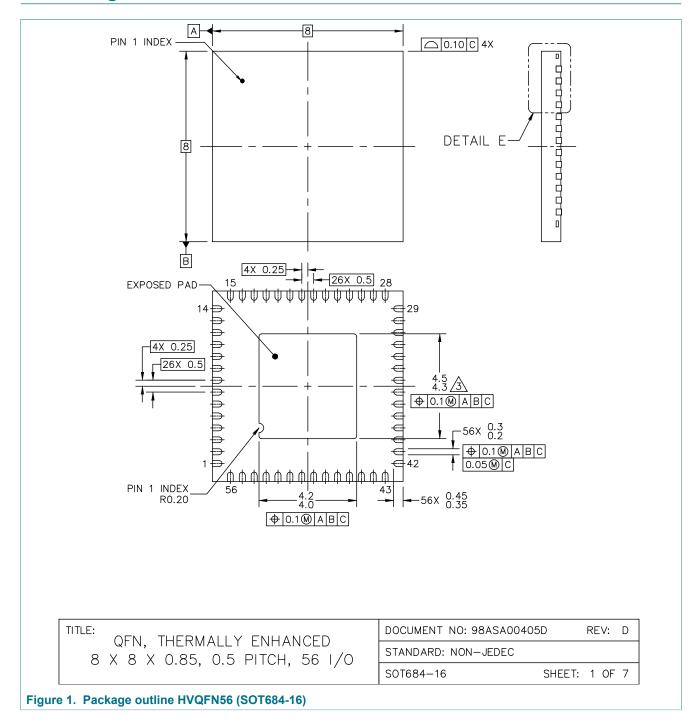
Table 1. Package summary

| Parameter                      | Min | Nom  | Max | Unit |
|--------------------------------|-----|------|-----|------|
| package length                 | -   | 8    | -   | mm   |
| package width                  | -   | 8    | -   | mm   |
| seated height                  | -   | 0.85 | -   | mm   |
| nominal pitch                  | -   | 0.5  | -   | mm   |
| actual quantity of termination | -   | 56   | -   |      |

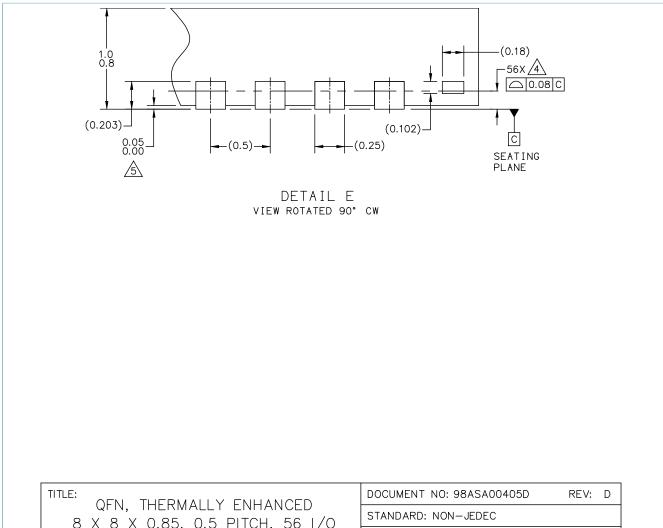


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# 2 Package outline



HVQFN56, plastic, thermally enhanced very thin quad flat, non-leaded package; 56 terminals; 0.5 mm pitch; 8 mm x 8 mm x 0.85 mm body



8 X 8 X 0.85, 0.5 PITCH, 56 I/O

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Figure 2. Package outline detail E of HVQFN56 (SOT684-16)

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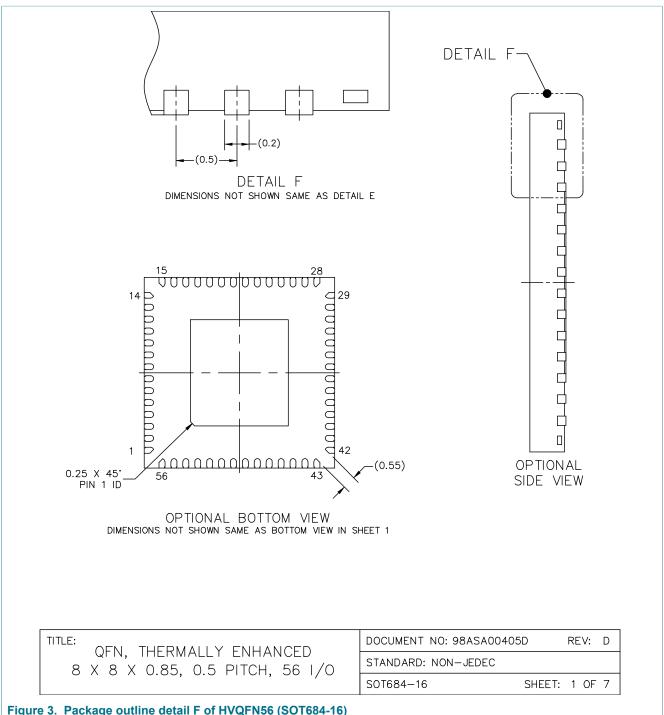


Figure 3. Package outline detail F of HVQFN56 (SOT684-16)

HVQFN56, plastic, thermally enhanced very thin quad flat, non-leaded package; 56 terminals; 0.5 mm pitch; 8 mm x 8 mm x 0.85 mm body

### 3 Soldering

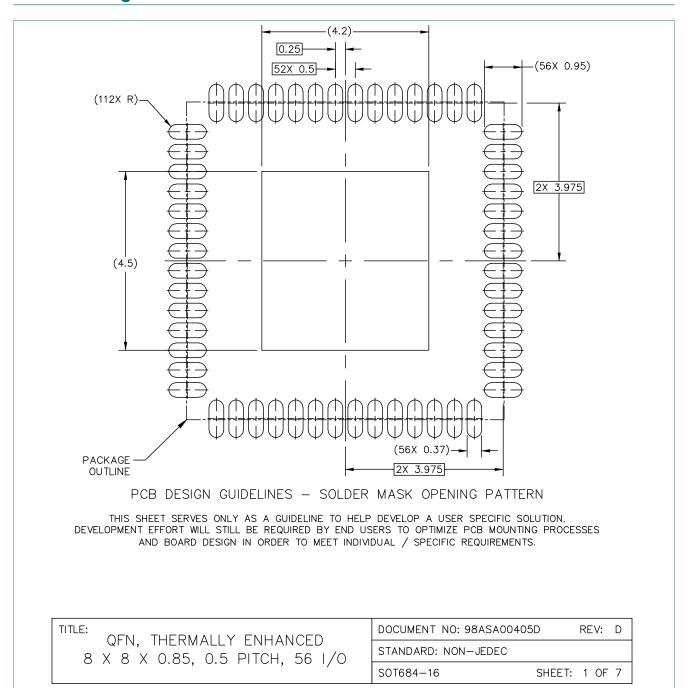


Figure 4. Reflow soldering footprint part1 for HVQFN56 (SOT684-16)

HVQFN56, plastic, thermally enhanced very thin quad flat, non-leaded package; 56 terminals; 0.5 mm pitch; 8 mm x 8 mm x 0.85 mm body

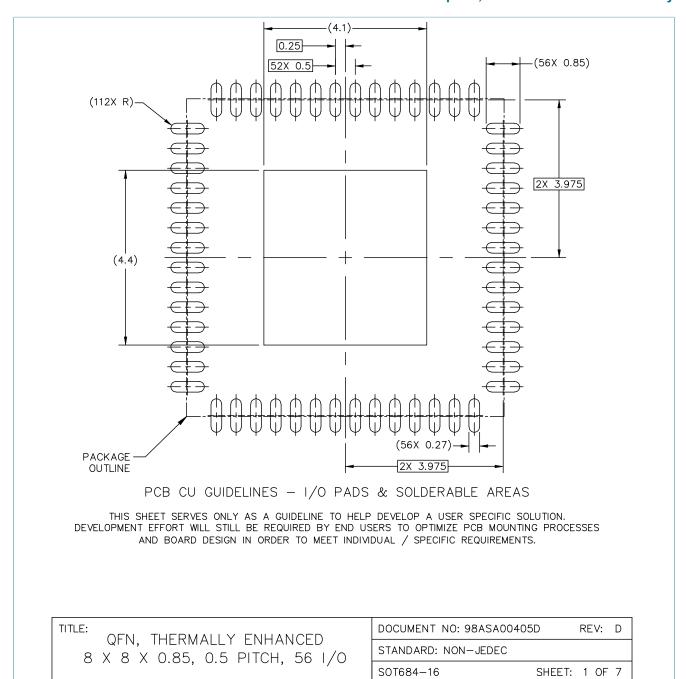
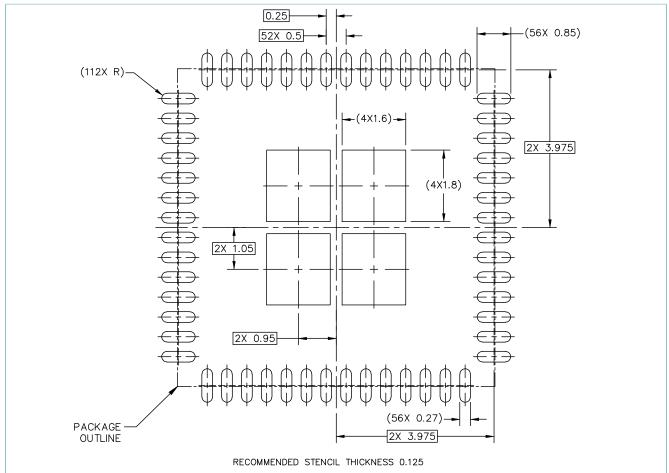


Figure 5. Reflow soldering footprint part2 for HVQFN56 (SOT684-16)

HVQFN56, plastic, thermally enhanced very thin quad flat, non-leaded package; 56 terminals; 0.5 mm pitch; 8 mm x 8 mm x 0.85 mm body



PCB DESIGN GUIDELINES - SOLDER PASTE STENCIL

THIS SHEET SERVES ONLY AS A GUIDELINE TO HELP DEVELOP A USER SPECIFIC SOLUTION.

DEVELOPMENT EFFORT WILL STILL BE REQUIRED BY END USERS TO OPTIMIZE PCB MOUNTING PROCESSES

AND BOARD DESIGN IN ORDER TO MEET INDIVIDUAL / SPECIFIC REQUIREMENTS.

TITLE:

QFN, THERMALLY ENHANCED

8 X 8 X 0.85, 0.5 PITCH, 56 I/O

DOCUMENT NO: 98ASA00405D REV: D

STANDARD: NON-JEDEC

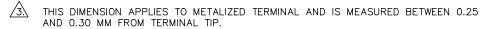
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Figure 6. Reflow soldering footprint part3 for HVQFN56 (SOT684-16)

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#### NOTE:

- 1. ALL DIMENSIONS ARE IN MILLIMETERS.
- 2. DIMENSIONING & TOLERANCING PER ASME Y14.5 1994.



BILATERAL COPLANARITY ZONE APPLIES TO THE EXPOSED PAD AS WELL AS THE TERMINALS.

 $\stackrel{\textstyle \checkmark}{\sim}$  this dimension applies only for terminals.

QFN, THERMALLY ENHANCED

8 X 8 X 0.85, 0.5 PITCH, 56 I/O

DOCUMENT NO: 98ASA00405D REV: D

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Figure 7. Package outline note HVQFN56 (SOT684-16)

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# 4 Legal information

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